

Table 4 Group A Inspection

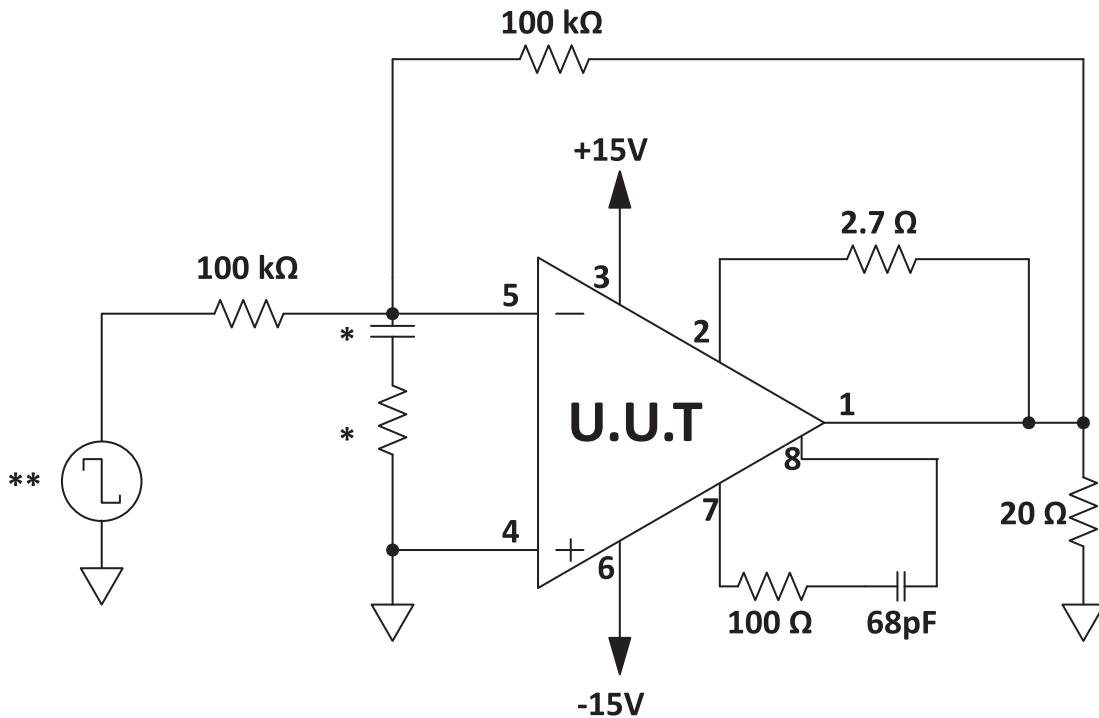
SG	Parameter ***	Symbol	Temp.	Power	Test Conditions	Min	Max	Units
1	Quiescent Current	I_Q	25°C	±150V	$V_{IN} = 0, A_V = 100$		25	mA
1	Input Offset Voltage	V_{OS}	25°C	±15V	$V_{IN} = 0, A_V = 100$		±4	mV
1	Input Offset Voltage	V_{OS}	25°C	±150V	$V_{IN} = 0, A_V = 100$		±2	mV
1	Input Bias Current, +IN	$+I_B$	25°C	±150V	$V_{IN} = 0$		±50	pA
1	Input Bias Current, -IN	$-I_B$	25°C	±150V	$V_{IN} = 0$		±50	pA
1	Input Offset Current	I_{OS}	25°C	±150V	$V_{IN} = 0$		±100	pA
3	Quiescent Current	I_Q	-55°C	±150V	$V_{IN} = 0, A_V = 100$		28	mA
3	Input Offset Voltage	V_{OS}	-55°C	±15V	$V_{IN} = 0, A_V = 100$		±6.4	mV
3	Input Offset Voltage	V_{OS}	-55°C	±150V	$V_{IN} = 0, A_V = 100$		±4.4	mV
3	Input Bias Current, +IN	$+I_B$	-55°C	±150V	$V_{IN} = 0$		±50	pA
3	Input Bias Current, -IN	$-I_B$	-55°C	±150V	$V_{IN} = 0$		±50	pA
3	Input Offset Current	I_{OS}	-55°C	±150V	$V_{IN} = 0$		±50	pA
2	Quiescent Current	I_Q	125°C	±150V	$V_{IN} = 0, A_V = 100$		28	mA
2	Input Offset Voltage	V_{OS}	125°C	±15V	$V_{IN} = 0, A_V = 100$		±7	mV
2	Input Offset Voltage	V_{OS}	125°C	±150V	$V_{IN} = 0, A_V = 100$		±5	mV
2	Input Bias Current, +IN	$+I_B$	125°C	±150V	$V_{IN} = 0$		±10	nA
2	Input Bias Current, -IN	$-I_B$	125°C	±150V	$V_{IN} = 0$		±10	nA
2	Input Offset Current	I_{OS}	125°C	±150V	$V_{IN} = 0$		±10	nA
4	Output Voltage, $I_O = 200mA$	V_O	25°C	±50V	$R_L = 200 \Omega$	40		V
4	Output Voltage, $I_O = 70mA$	V_O	25°C	±150V	$R_L = 2 k\Omega$	141		V
4	Output Voltage, $I_O = 20mA$	V_O	25°C	±48V	$R_L = 2 k\Omega$	40		V
4	Current Limits	I_{CL}	25°C	±50V	$R_{CL} = 10 \Omega, R_L = 200 \Omega$	60	112	A
4	Stability/Noise	E_N	25°C	±150V	$C_C = 68pF, R_C = 100 \Omega, A_V = +1, C_L = 470pF$		1	mV
4	Slew Rate	SR	25°C	±150V	$R_L = 2 k\Omega, A_V = 100, C_C = OPEN$	400		V/ μs
4	Open Loop Gain	A_{OL}	25°C	±150V	$R_L = 2 k\Omega, F = 15 Hz, C_C = OPEN$	96		dB
4	Common Mode Rejection	CMR	25°C	±150V	$F = DC, V_{CM} = \pm 90V$	90		dB

PA85M

SG	Parameter	Symbol	Temp.	Power	Test Conditions	Min	Max	Units
6	Output Voltage, $I_O = 200\text{mA}$	V_O	-55°C	$\pm 50\text{V}$	$R_L = 200\ \Omega$	40		V
6	Output Voltage, $I_O = 70\text{mA}$	V_O	-55°C	$\pm 150\text{V}$	$R_L = 2\ \text{k}\Omega$	141		V
6	Output Voltage, $I_O = 20\text{mA}$	V_O	-55°C	$\pm 48\text{V}$	$R_L = 2\ \text{k}\Omega$	40		V
6	Stability/Noise	E_N	-55°C	$\pm 150\text{V}$	$C_C = 68\text{pF}$, $R_C = 100\ \Omega$, $A_V = +1$, $C_L = 470\text{pF}$		1	mV
6	Slew Rate	SR	-55°C	$\pm 150\text{V}$	$R_L = 2\ \text{k}\Omega$, $A_V = 100$, $C_C = \text{OPEN}$	400		V/ μs
6	Open Loop Gain	A_{OL}	-55°C	$\pm 150\text{V}$	$R_L = 2\ \text{k}\Omega$, $F = 15\ \text{Hz}$, $C_C = \text{OPEN}$	96		dB
6	Common Mode Rejection	CMR	-55°C	$\pm 150\text{V}$	$F = \text{DC}$, $V_{CM} = \pm 90\text{V}$	90		dB
5	Output Voltage, $I_O = 150\text{mA}$	V_O	125°C	$\pm 40\text{V}$	$R_L = 200\ \Omega$	30		V
5	Output Voltage, $I_O = 70\text{mA}$	V_O	125°C	$\pm 150\text{V}$	$R_L = 2\ \text{k}\Omega$	141		V
5	Output Voltage, $I_O = 20\text{mA}$	V_O	125°C	$\pm 48\text{V}$	$R_L = 2\ \text{k}\Omega$	40		V
5	Stability/Noise	E_N	125°C	$\pm 150\text{V}$	$C_C = 68\text{pF}$, $R_C = 100\ \Omega$, $A_V = +1$, $C_L = 470\text{pF}$		1	mV
5	Slew Rate	SR	125°C	$\pm 150\text{V}$	$R_L = 2\ \text{k}\Omega$, $A_V = 100$, $C_C = \text{OPEN}$	400		V/ μs
5	Open Loop Gain	A_{OL}	125°C	$\pm 150\text{V}$	$R_L = 2\ \text{k}\Omega$, $F = 15\ \text{Hz}$, $C_C = \text{OPEN}$	96		dB
5	Common Mode Rejection	CMR	125°C	$\pm 150\text{V}$	$F = \text{DC}$, $V_{CM} = \pm 90\text{V}$	90		dB

BURN IN CIRCUIT

Figure 1: Burn In Circuit



- * These components are used to stabilize device due to poor high frequency characteristics of burn in board.
- ** Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.
- *** An additional test is performed manually at $T_C = 25^\circ\text{C}$ which stresses power supply, common mode range and output swing to $\pm 225\text{V}$ (450V total).

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