

Table 4 Group A Inspection

SG	Parameter	Symbol	Temp.	Power	Test Conditions	Min	Max	Units
1	Quiescent Current	I_Q	25°C	±15V	$V_{IN}=0, A_V=100, R_{CL}=0.2 \Omega$		40	mA
1	Input Offset Voltage	V_{OS}	25°C	±15V	$V_{IN} = 0, A_V = 100$		10	mV
1	Input Offset Voltage	V_{OS}	25°C	±7V	$V_{IN} = 0, A_V = 100$		11.6	mV
1	Input Offset Voltage	V_{OS}	25°C	±19V	$V_{IN} = 0, A_V = 100$		10.8	mV
1	Input Bias Current, +IN	$+I_B$	25°C	±15V	$V_{IN} = 0$		200	pA
1	Input Bias Current, -IN	$-I_B$	25°C	±15V	$V_{IN} = 0$		200	pA
1	Input Offset Current	I_{OS}	25°C	±15V	$V_{IN} = 0$		100	pA
3	Quiescent Current	I_Q	-55°C	±15V	$V_{IN}=0, A_V=100, R_{CL}=0.2 \Omega$		60	mA
3	Input Offset Voltage	V_{OS}	-55°C	±15V	$V_{IN} = 0, A_V = 100$		14	mV
3	Input Offset Voltage	V_{OS}	-55°C	±7V	$V_{IN} = 0, A_V = 100$		15.6	mV
3	Input Offset Voltage	V_{OS}	-55°C	±19V	$V_{IN} = 0, A_V = 100$		14.8	mV
3	Input Bias Current, +IN	$+I_B$	-55°C	±15V	$V_{IN} = 0$		200	pA
3	Input Bias Current, -IN	$-I_B$	-55°C	±15V	$V_{IN} = 0$		200	pA
3	Input Offset Current	I_{OS}	-55°C	±15V	$V_{IN} = 0$		100	pA
2	Quiescent Current	I_Q	125°C	±15V	$V_{IN}=0, A_V=100, R_{CL}=0.2 \Omega$		60	mA
2	Input Offset Voltage	V_{OS}	125°C	±15V	$V_{IN} = 0, A_V = 100$		15	mV
2	Input Offset Voltage	V_{OS}	125°C	±7V	$V_{IN} = 0, A_V = 100$		16.6	mV
2	Input Offset Voltage	V_{OS}	125°C	±19V	$V_{IN} = 0, A_V = 100$		15.8	mV
2	Input Bias Current, +IN	$+I_B$	125°C	±15V	$V_{IN} = 0$		30	nA
2	Input Bias Current, -IN	$-I_B$	125°C	±15V	$V_{IN} = 0$		30	nA
2	Input Offset Current	I_{OS}	125°C	±15V	$V_{IN} = 0$		10	nA
4	Output Voltage, $I_O = 5A$	V_O	25°C	±9V	$R_L = 1 \Omega, R_{CL} = 0 \Omega$	5		V
4	Output Voltage, $I_O = 36mA$	V_O	25°C	±19V	$R_L = 500 \Omega$	18		V
4	Output Voltage, $I_O = 2A$	V_O	25°C	±12V	$R_L = 5 \Omega, R_{CL} = 0 \Omega$	10		V
4	Current Limits	I_{CL}	25°C	±9V	$R_L = 5 \Omega, R_{CL} = 1 \Omega$	0.54	0.86	A
4	Stability/Noise	E_N	25°C	±15V	$R_L=500 \Omega, A_V=1, C_L=1.5nF$		1	mV
4	Slew Rate	SR	25°C	±18V	$R_L = 500 \Omega$	13	100	V/ μ s
4	Open Loop Gain	A_{OL}	25°C	±15V	$R_L = 500 \Omega, F = 10 \text{ Hz}$	86		dB
4	Common Mode Rejection	CMR	25°C	±8.25V	$R_L = 500 \Omega, F = \text{DC},$ $V_{CM} = \pm 2.25V$	70		dB

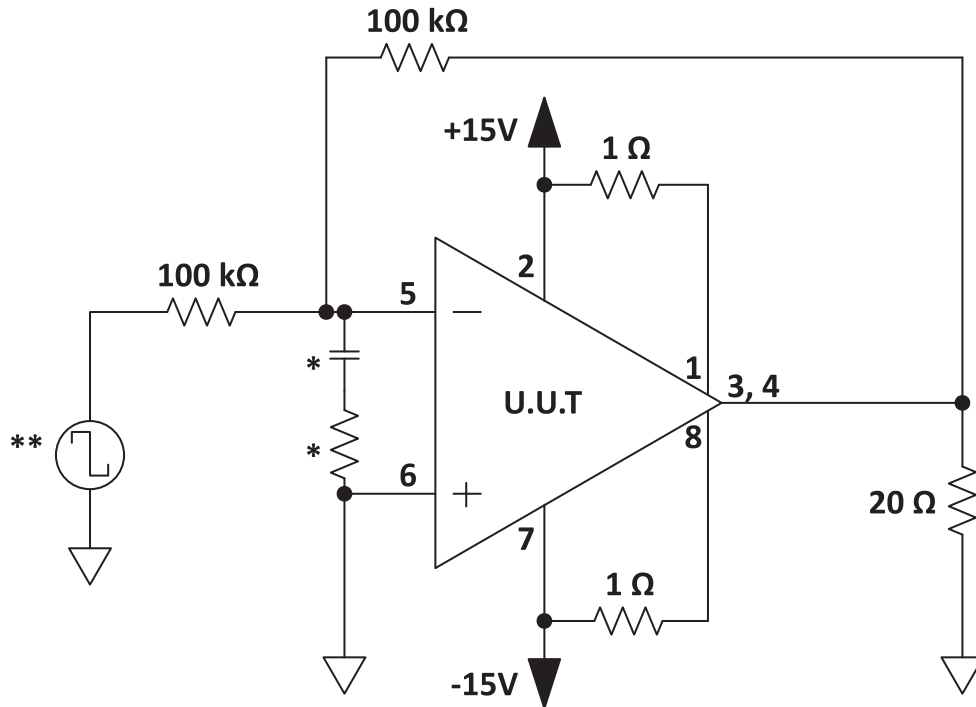
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SG	Parameter	Symbol	Temp.	Power	Test Conditions	Min	Max	Units
6	Output Voltage, $I_O = 5A$	V_O	$-55^{\circ}C$	$\pm 9V$	$R_L = 1 \Omega, R_{CL} = 0 \Omega$	5		V
6	Output Voltage, $I_O = 36mA$	V_O	$-55^{\circ}C$	$\pm 19V$	$R_L = 500 \Omega$	18		V
6	Output Voltage, $I_O = 2A$	V_O	$-55^{\circ}C$	$\pm 12V$	$R_L = 5 \Omega, R_{CL} = 0 \Omega$	10		V
6	Stability/Noise	E_N	$-55^{\circ}C$	$\pm 15V$	$R_L = 500 \Omega, A_V = 1, C_L = 1.5nF$		1	mV
6	Slew Rate	SR	$-55^{\circ}C$	$\pm 18V$	$R_L = 500 \Omega$	13	100	V/ μs
6	Open Loop Gain	A_{OL}	$-55^{\circ}C$	$\pm 15V$	$R_L = 500 \Omega, F = 10 \text{ Hz}$	86		dB
6	Common Mode Rejection	CMR	$-55^{\circ}C$	$\pm 8.25V$	$R_L = 500 \Omega, F = DC,$ $V_{CM} = \pm 2.25V$	70		dB
5	Output Voltage, $I_O = 3A$	V_O	$125^{\circ}C$	$\pm 7V$	$R_L = 1 \Omega, R_{CL} = 0 \Omega$	3		V
5	Output Voltage, $I_O = 36mA$	V_O	$125^{\circ}C$	$\pm 19V$	$R_L = 500 \Omega$	18		V
5	Output Voltage, $I_O = 2A$	V_O	$125^{\circ}C$	$\pm 12V$	$R_L = 5 \Omega, R_{CL} = 0 \Omega$	10		V
5	Stability/Noise	E_N	$125^{\circ}C$	$\pm 15V$	$R_L = 500 \Omega, A_V = 1, C_L = 1.5nF$		1	mV
5	Slew Rate	SR	$125^{\circ}C$	$\pm 18V$	$R_L = 500 \Omega$	8.5	100	V/ μs
5	Open Loop Gain	A_{OL}	$125^{\circ}C$	$\pm 15V$	$R_L = 500 \Omega, F = 10 \text{ Hz}$	86		dB
5	Common Mode Rejection	CMR	$125^{\circ}C$	$\pm 8.25V$	$R_L = 500 \Omega, F = DC,$ $V_{CM} = \pm 2.25V$	70		dB

BURN IN CIRCUIT

Figure 1: Burn In Circuit



*These components are used to stabilize device due to poor high frequency characteristics of burn in board.
 **Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.

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